THE COMMISSIONER OF PATENTS AND TRADEMARKS WASHINGTON, D.C. 20231

Sir:

Transmitted herewith for filing is the Utility patent application of:



INVENTOR:	Hirokatsu HAYASHI and Toshiro TAKAHASHI				
For:	LEVEL SHIFT CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT				
FOR:					

Enclosed are:

- 10 sheets of □ formal □ informal drawings.
- ☑ Notice to claim priority date of October 30, 2000.
- ⋈ A preliminary amendment.
- ☐ Applicant requests non-publication.
- ☑ An Information Disclosure Statement with PTO Form 1449.
- Mac An assignment of the invention to Hitachi, Ltd. and Hitachi ULSI Systems Co., Ltd.
- ☑ An Executed Declaration
- \boxtimes The filing fee is calculated as shown below:

For	Numbe	R FILED	Number Extra	RATE	CALCULATIONS	
TOTAL CLAIMS	22	-20	2	x\$18 =	36.00	
INDEPENDENT CLAIMS	5	-3	2	x\$84 =	168.00	
MULTIPLE DEPENDENT CLAIM(S) (if applicable) 1 +\$280 =					\$280.00	
			BASIC FEE		\$740.00	
			TOTAL OF ABOVE		1,224.00	
REDUCTION BY ½ FOR FILING BY SMALL ENTITY (note 37 C.F.R. §§ 1.9, 1 27, 1.28). IF APPLICABLE, VERIFIED STATEMENT MUST BE ATTACHED						
				TOTAL	1,224.00	

Please charge my Deposit Account Number	in the amount of	to cover the filing and assignment recordation
fees. A duplicate copy of this paper is enclosed.		

- \boxtimes A check in the amount of \$1,264.00 to cover the filing fee and assignment recordation fee is enclosed.
- The Commissioner is hereby authorized to charge any additional fees associated with this communication, including patent \boxtimes application filing fees and processing fees under 37 C.F.R. § 1.16 and 1.17, or credit any overpayment to Deposit Account Number 08-1480.

Respectfully submitted

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October 15, 2001